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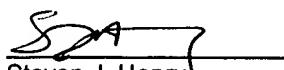
ATTORNEY'S DOCKET NO: G0631/7010 (SJH)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Seamus Paul Whiston et al  
Serial No: 09/480,223  
Confirmation No.: 4079  
Filed: January 10, 2000  
For: METHOD FOR FORMING A DMOS DEVICE AND A DMOS  
DEVICE  
  
Examiner: Y.B. Huynh  
Art Unit: 2813

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Commissioner for Patents, Washington, D.C. 20231, on the 12th day of July, 2002.

  
Steven J. Henry  
Reg. No. 27,900

COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

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Transmitted herewith are the following documents:

- Amendment
- Petition for Three Month Extension of Time
- Return Receipt Postcard

If the enclosed papers are considered incomplete, the Mail Room and/or the Application Branch is respectfully requested to contact the undersigned at (617)720-3500, Boston, Massachusetts.

A check for \$920.00 is enclosed for the one-month extension of time. If the fee is insufficient, the balance may be charged to Deposit Account No. 23/2825. A duplicate of this sheet is enclosed.

Respectfully Submitted,

  
Steven J. Henry  
Reg. No. 27,900  
Wolf, Greenfield & Sacks, P.C.  
600 Atlantic Avenue  
Boston, MA 02210-2211  
(617) 720-3500

Docket No. G0631/7010  
Date: July 12, 2002  
x07/31/02



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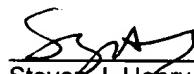
ATTORNEY'S DOCKET NO: G0631/7010 (SJH)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Seamus Paul Whiston et al  
Serial No: 09/480,223  
Confirmation No.: 4079  
Filed: January 10, 2000  
For: METHOD FOR FORMING A DMOS DEVICE AND A DMOS DEVICE  
Examiner: N. Richards  
Art Unit: 2815

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

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Steven J. Henry  
Reg. No. 27,900

COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

Sir:

**AMENDMENT**

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In response to the Official Action mailed January 31, 2002, kindly amend the above-identified application as follows:

**IN THE CLAIMS:**

Please amend claims 1 and 13 as follows (a marked-up copy of the amended claims is attached):

1. (Amended) A method for forming a body region in a drain region of a DMOS device on a wafer after a gate has been formed with the body region extending partly beneath the gate of the DMOS device and appropriately aligned with the gate, the drain region defining a surface plane, the method comprising the steps of: